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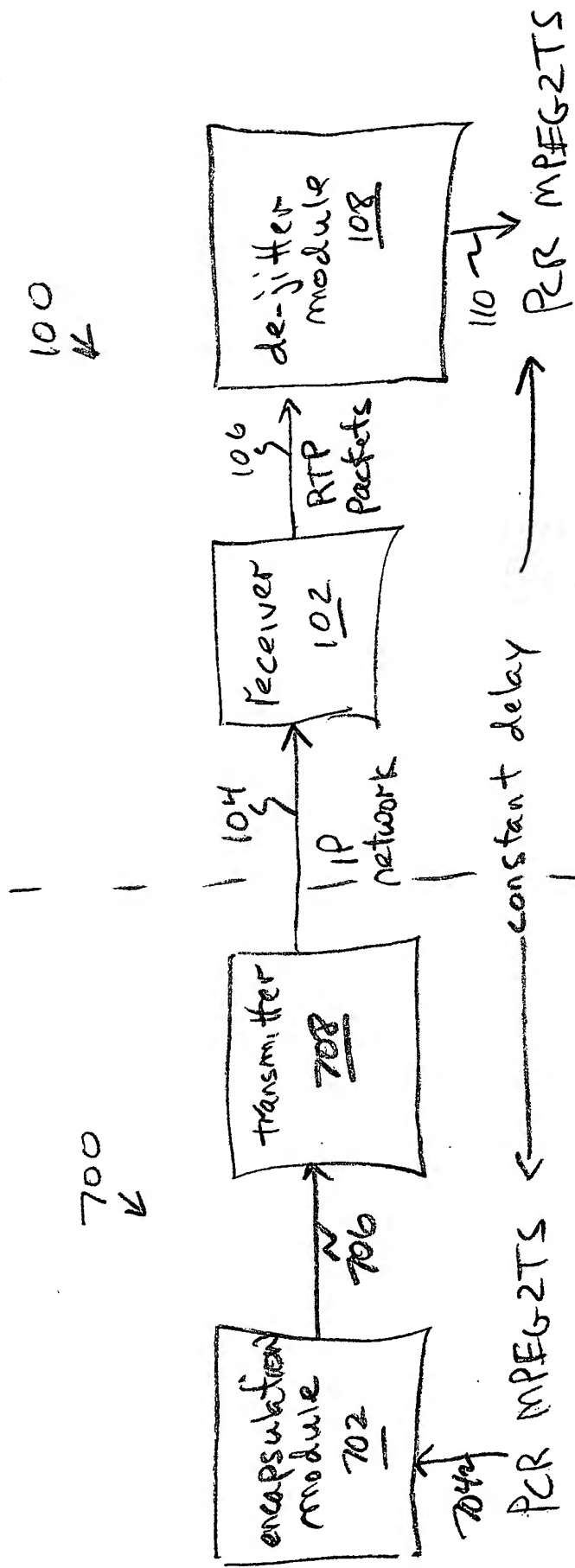
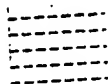


Fig. 1

Fig. 7

V=2	PX	CC	M	PT	SN
Timestamp (32 bits)					
SSRC-ID					

RH	MPEG2TS
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RH	MPEG2TS
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RH	PCR MPEG2TS
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RH	MPEG2TS
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Fig. 2

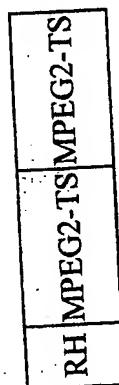
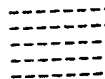
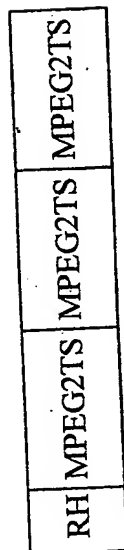
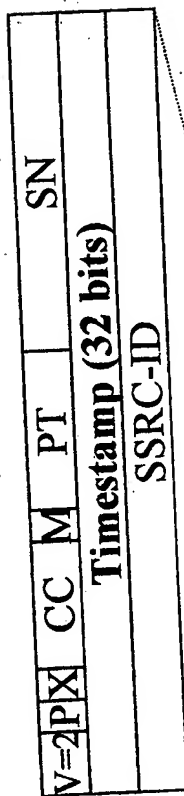


Fig. 3

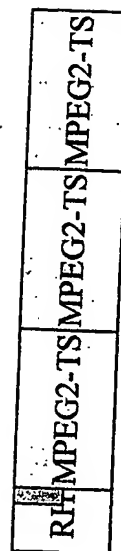
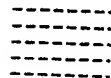
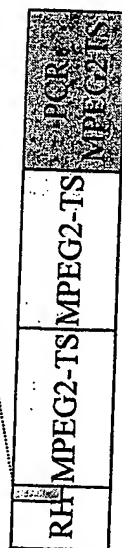
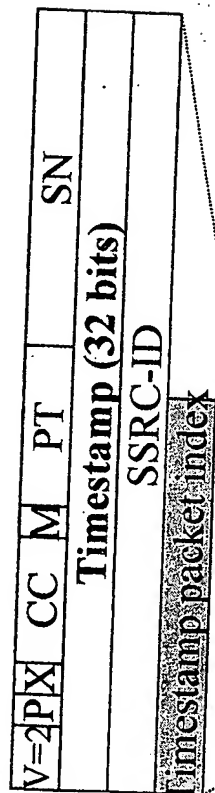


Fig. 4

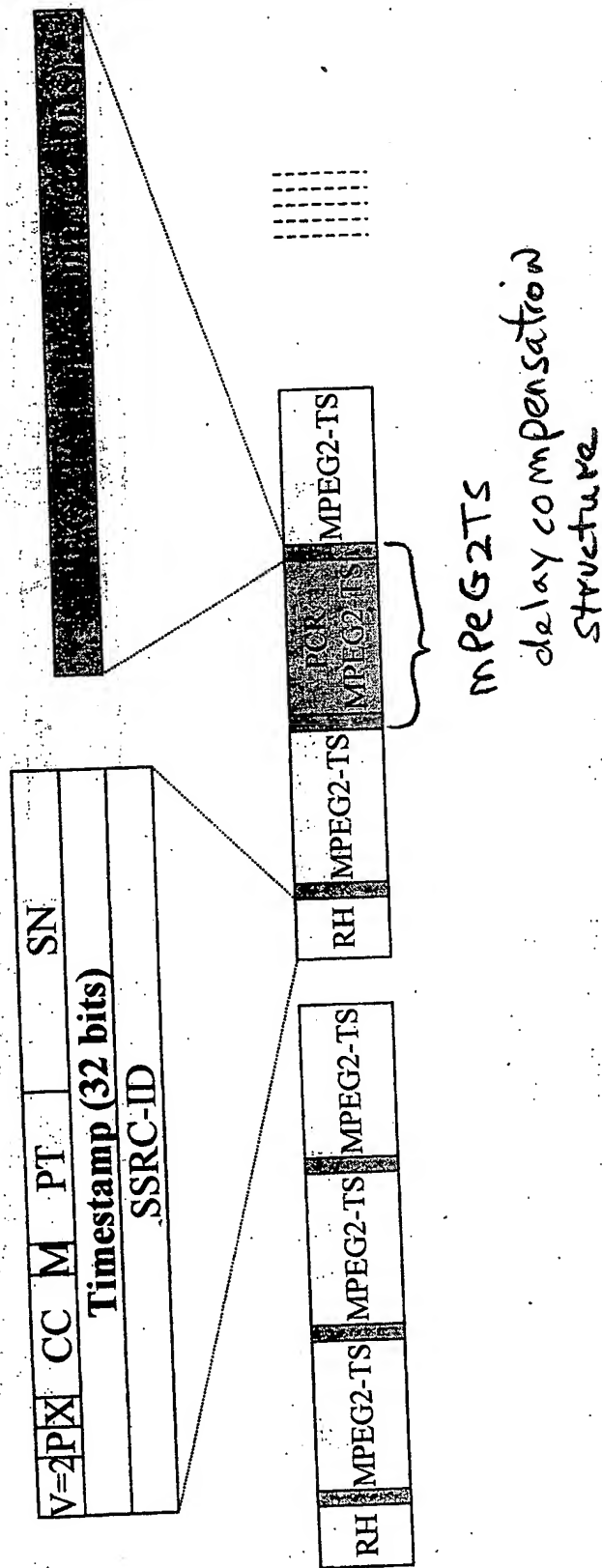


Fig. 6

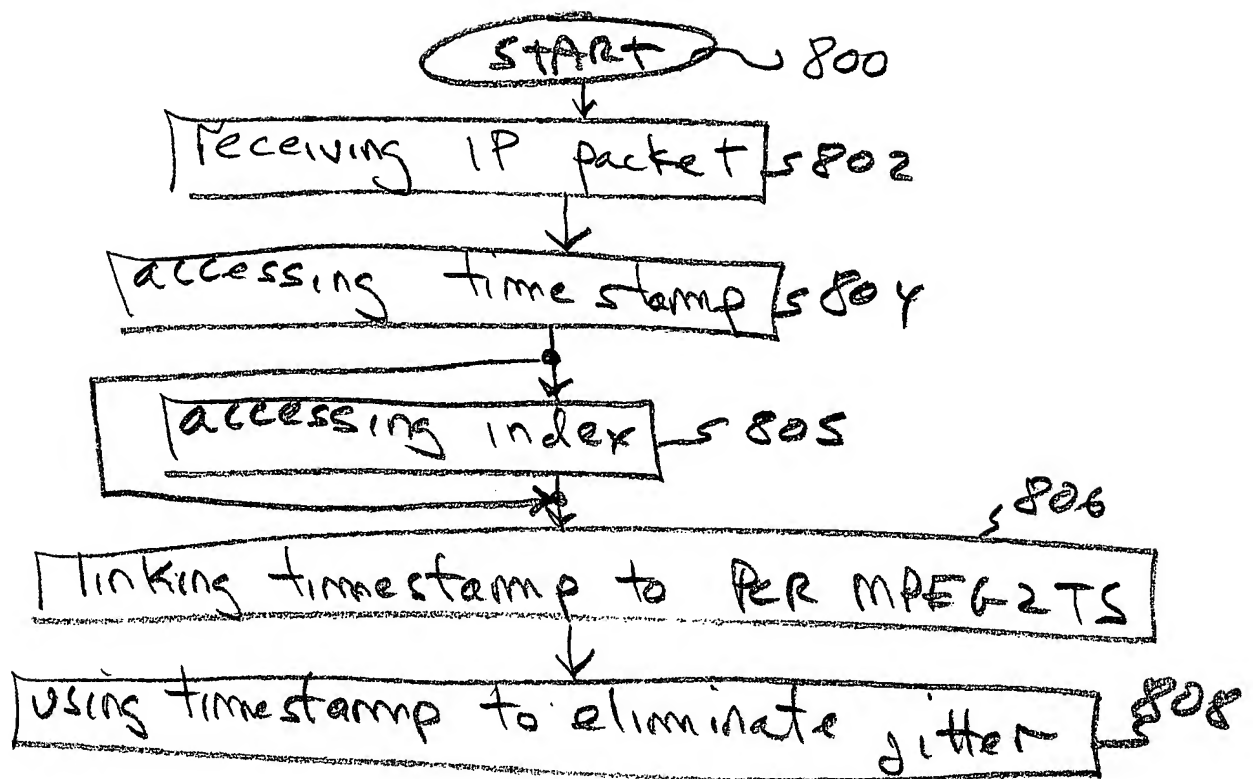


Fig. 8

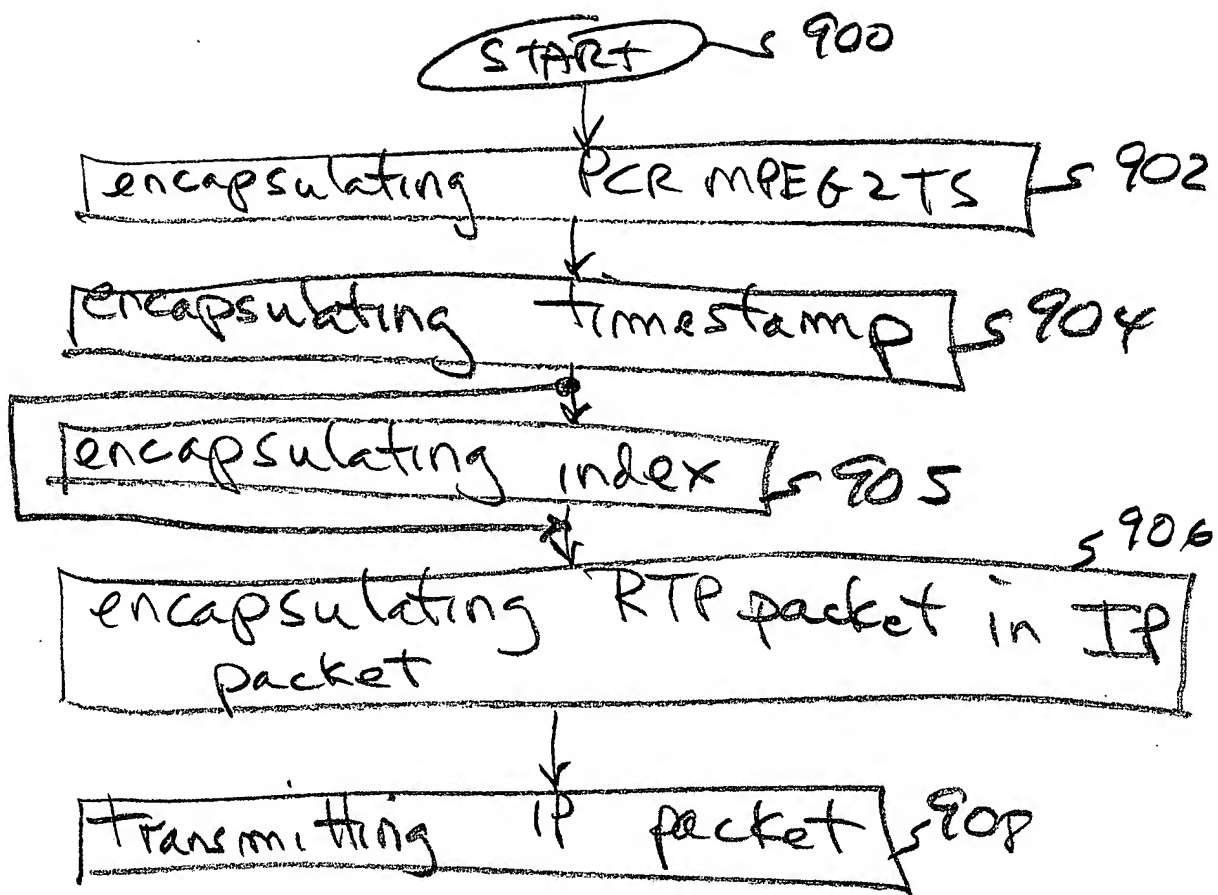
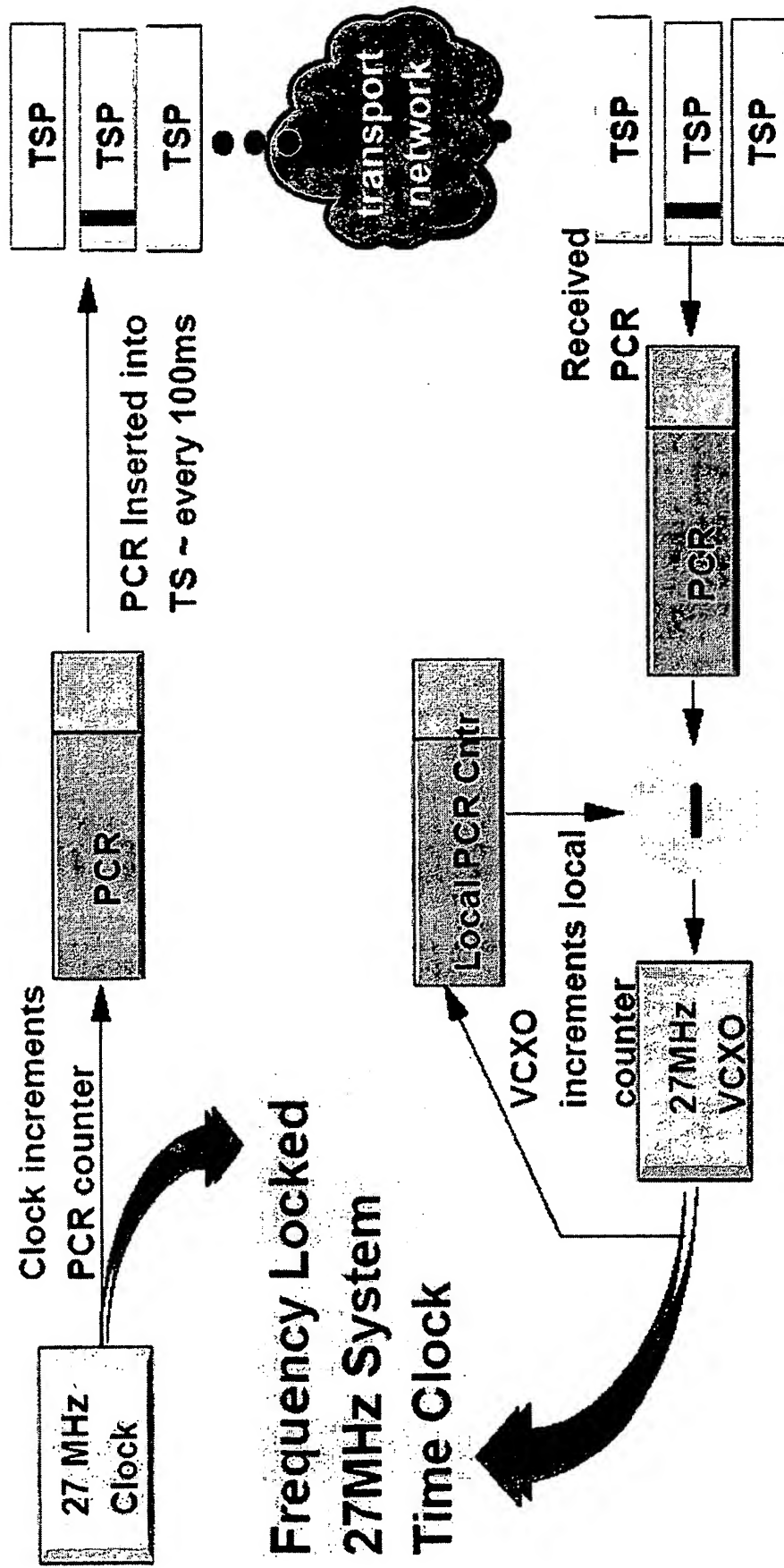


Fig. 9



System clock frequency = 27M hz +/- 540 hz

Fig. 10 (prior art)

Timing model of MPEG system

■ From ISO/IEC 13818-1

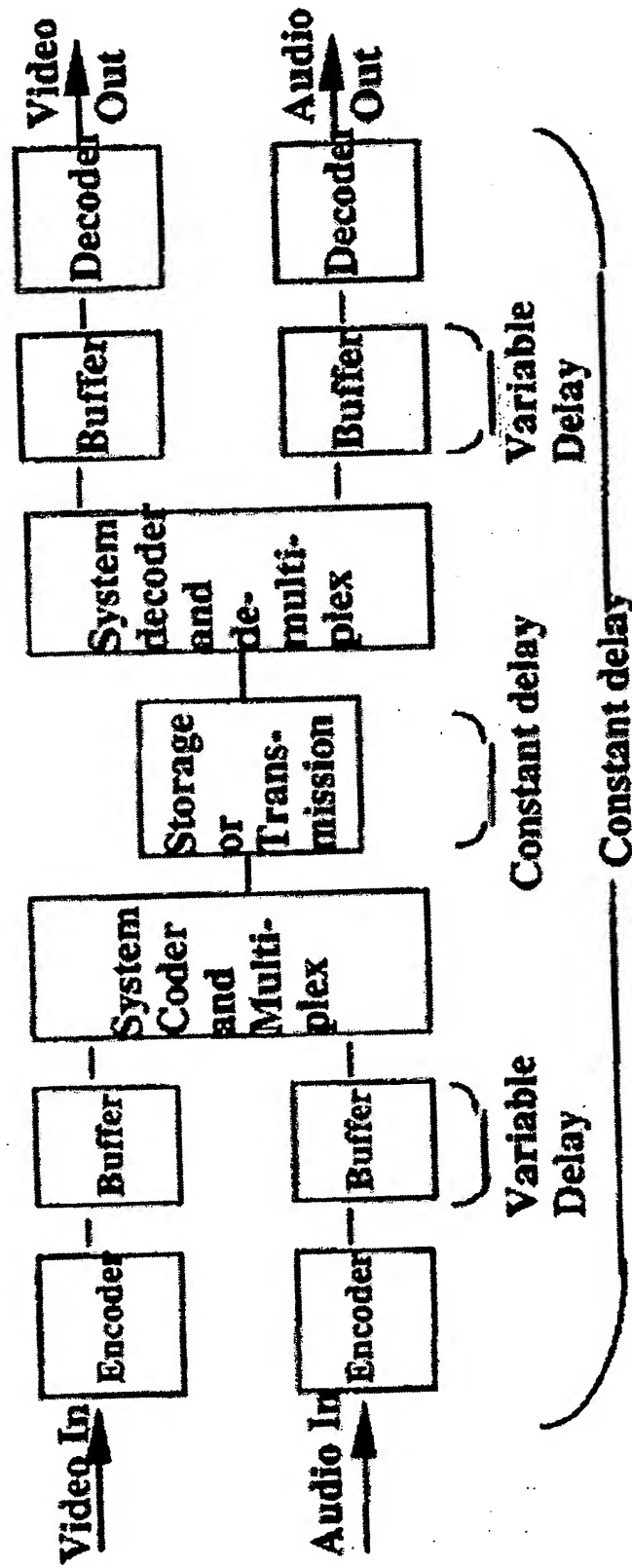
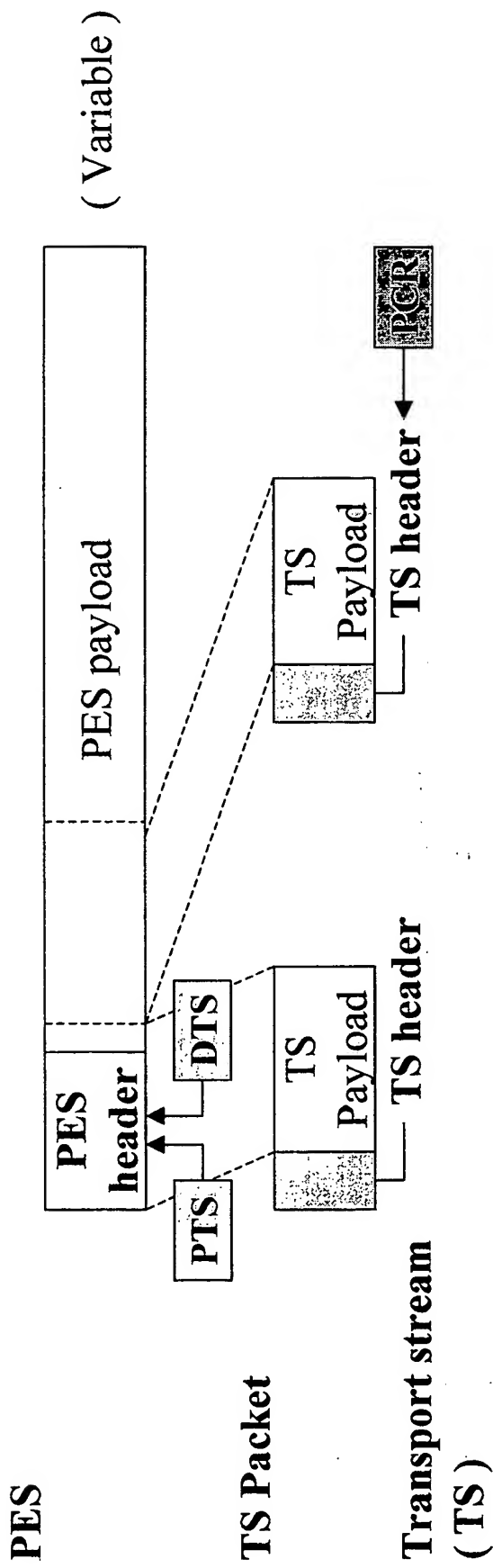


Fig. 11 (Prior art)

Fig. 12 (prior art)

MPEG2TS packet structure



ISO/IEC 13818 requires that **PCR** information should be inserted to MPEG2TS packet at least once per 100ms. **NOT** in every TS packet.
PES: packetized element stream

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